



copy
Docket No. : SON-2778
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. 6,801,074 B2

Inventor: Tetsumasa MEGURO

Patented: October 5, 2004

ATTN: CORRECTIONS BRANCH

For: CLOCK SWITCHING CIRCUIT

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
DEC 15 2004
of Correction

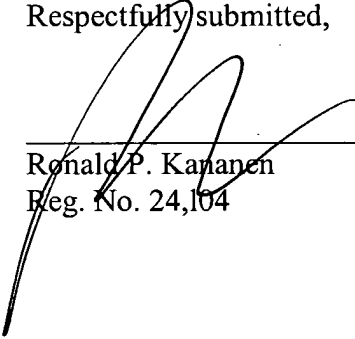
Sir:

It is respectfully requested that a Certificate of Correction be issued in connection with the above-identified patent. It appears that a mistake(s) was recorded through the fault of the Patent and Trademark Office in the printing of the patent as clearly disclosed by the records of the Office within the meaning of 35 USC § 254. Accordingly, two copies of the special Certificate of Correction are attached hereto. It is believed that the error(s) noted is an error(s) of consequence involving claim language and thus warrants the granting of a Certificate of Correction. It is believed that the error(s) were made on the part of the Office and that no government fee is required.

Should any costs be incurred, please consider this authorization to charge Deposit Account No. 18-0013.

Respectfully submitted,

Dated: December 13, 2004



Ronald P. Kananen
Reg. No. 24,104

RADER, FISHMAN & GRAUER PLLC
Suite 501, Lion Building
1233 20th Street, N.W.
Washington, D.C. 20036
(202) 955-3750

17 DEC 2004

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Patent No. 6,801,074 B2

Dated: October 5, 2004

Inventor (s): Tetsumasa MEGURO

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12

Line 37, "delay clock" should read - - delays clock - - .

MAILING ADDRESS OF SENDER:

Customer No. 23353

Rader, Fishman & Grauer PLLC

1233 20th Street, NW

Suite 501

Washington, DC 20036

Patent No. US 6,801,074 B2

FORM PTO 1050 (REV. 3-82) SON-2778

7 DEC 2004

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Patent No. 6,801,074 B2

Dated: October 5, 2004

Inventor (s): Tetsumasa MEGURO

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12

Line 37, "delay clock" should read - - delays clock - - .

MAILING ADDRESS OF SENDER:

Customer No. 23353

Rader, Fishman & Grauer PLLC

1233 20th Street, NW

Suite 501

Washington, DC 20036

Patent No. US 6,801,074 B2

FORM PTO 1050 (REV. 3-82) SON-2778

17 DEC 2004